

	L #	Hits	Search Text	DBs	Time Stamp
1	L1	54	(differential adj2 transistor\$1 same current adj mirror same capacit\$5) and second adj2 current adj mirror	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 14:11
2	L2	80	(differential adj2 transistor\$1 same current adj mirror) and second adj2 current adj mirror and output near3 capacit\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:19
3	L3	48	2 not 1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:16
4	L4	277	(differential adj2 transistor\$1 same current adj mirror) and output near3 capacit\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:19
5	L5	197	4 not 2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:31
6	L6	174	(clock or signal) adj detect\$5 and differential adj2 (transistor\$1 or amplif\$6) and output near2 capacit\$5 and current adj mirror	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:32
7	L7	166	6 not (4 or 2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:33
8	L8	4150	327/51,52,53,65,66,70,560,561,562,563.ccls. or 323/315,316.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 10:30
9	L9	1921	8 and current adj mirror	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 10:31
10	L10	594	9 and differential adj2 (input\$1 or clock\$1 or signal\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 10:32
11	L11	573	10 not (6 or 4 or 2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 10:33
12	L12	67	11 and output near4 capacitor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 10:41

	L #	Hits	Search Text	DBs	Time Stamp
13	L13	506	11 not 12	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 10:41
14	L14	158	13 and detect\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 11:07
15	L15	348	13 not 14	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 11:31
16	L16	20	6281699.pn. 6016566.pn. 5894284.pn. 4117348.pn. 4230958.pn. 6333646.pn. 4672325.pn. 5986497.pn. 4380080.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 11:34
17	L17	7694	differential adj2 (clock\$1 or signal\$1 or input\$1) with (monit\$5 or detect\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 11:35
18	L18	359	17 and current adj mirror and capacit\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 11:36
19	L19	332	18 not 13	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 11:36
20	L20	329	19 not 11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 11:36
21	L25	1	"6384653".PN.	USPAT	2004/04/28 13:41
22	L26	1	"6359510".PN.	USPAT	2004/04/28 13:41
23	L27	1	"6356152".PN.	USPAT	2004/04/28 13:42
24	L28	1	"6326846".PN.	USPAT	2004/04/28 13:42
25	L29	1	"6281751".PN.	USPAT	2004/04/28 13:43
26	L30	1	"5914638".PN.	USPAT	2004/04/28 13:43
27	L31	1	"5451898".PN.	USPAT	2004/04/28 13:44
28	L32	1	"5422529".PN.	USPAT	2004/04/28 13:44

	L #	Hits	Search Text	DBs	Time Stamp
29	L33	81	(differential adj2 (transistor\$1 or clock\$ or signal\$1) with capacit\$5) same current adj mirror	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 14:12
30	L34	64	33 not (1 or 2 or 3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 14:26
31	L35	84	common adj mode adj voltage with differential with (monitor\$5 or detect\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 14:27